

PSG College of Technology, Coimbatore - 641 004
Department of Electronics and Communication Engineering
in collaboration with

PSG Centre for Academic Research and Excellence (PSG CARE)

**IEEE Technically Co-Sponsored Second International Conference on
Intelligent Innovations in Engineering and Technology (ICIET-2025)**
4th - 6th December 2025

Program Schedule

Day 1 - 04.12.2025		
9.00 am–9.45 am	ICIET 25 Pre-conference Workshops - Inaugural Function Venue: New Admin Block Conference Hall https://meet.google.com/hpp-qqfi-dtq	
9.45 am – 10.00 am	Interaction Break	
10.00 am – 12.30 pm 12.30 pm to 1.30 pm (Networking Lunch Break) 1.30 pm to 4.30 pm	Pre-Conference Workshop 1 IIoT & Digital Twin Technology for Factory Automation Industry Experts: Mr. Sivaganesh Vellingiri, Founder and CEO, Grow <i>i</i> Tech, Coimbatore. & Mr. Gokulraja Pitchamuthu, Head Application Engineering, South Zone, FANUC India. Venue: PSG-TI Centre of Excellence for Signal Processing (E-Block 2 nd Floor)	Pre-Conference Workshop 2 AI For Digital VLSI Design and Verification Industry Expert: Mr. Guha Lakshmanan, Director, Power Switches India R&D Team, Texas Instruments, Bengaluru. Venue: SMDP C2S VLSI Design Centre (E-Block 2 nd Floor)

Day 2 - 05.12.2025

8.30 am - 09.30 am	Registration		
9.30 am - 10.30 am	ICIET 25 – Inaugural Ceremony Venue: D Block Ground Floor Conference Hall https://meet.google.com/hpp-gqfi-dtq		
	Chief Guest Shri. Sandeep Oswal, TI Senior Fellow, Technologist and Signal Chain Architect, Texas Instruments, Bengaluru.	Guest of Honor Dr. Shanmuganathan Raman, Professor and Head, Department of Computer Science and Engineering, Indian Institute of Technology, Gandhinagar.	
10.30am – 11.00am	Interaction Break		
11.00 am - 12.30pm	Keynote Session 1 (11.00 am - 11.45 am) Shri. Sandeep Oswal, TI Senior Fellow, Technologist and Signal Chain Architect, Texas Instruments, Bengaluru.. Title: Trends in Medical Systems and Engineering Venue: D Block Ground Floor Conference Hall https://meet.google.com/hpp-gqfi-dtq		Keynote Session 2 (11.45 am - 12.30 pm) Dr. Ramesh Bansal, Professor, Department of EEE, University of Sharjah, United Arab Emirates. Title: Renewable Energy Integration in Smart Grid Environment Venue: D Block Ground Floor Conference Hall https://meet.google.com/hpp-gqfi-dtq
	Networking Lunch Break		
1.45 pm - 4.30 pm	Technical Presentation 1		
	Session 1A Venue: F-202 (F-Block 1 st Floor)	Session 1B Venue: F-203 (F-Block 1 st Floor)	Session 1C Venue: PSG CARE Hall G203 (G-Block 1 st Floor)
	Session 1D Venue: PSG-TI Centre of Excellence for Signal Processing (E-Block 2 nd Floor)	Session 1E Venue: ECE Conference Hall (E-Block 2 nd Floor)	Session 1F Venue: PSG-Keysight Centre of Excellence for Advanced Wireless Technology (E-Block 2 nd Floor)

3.30pm - 4.15pm	<p>Keynote Session 3 Dr. Wasif Naseem, Professor, Department of Electrical Engineering and Computer Science, Queen's University Belfast, Belfast, Northern Ireland. Title: Uncertainty Aware Path Planning and Collision Avoidance for Marine Vehicles Venue: G-202 (G-Block 1st Floor) https://meet.google.com/hpp-qqfi-dtq</p>			
Day 3 - 06.12.2025				
9.00 am - 9.45 am	<p>Keynote Session 4 Dr. Fatih Porikli, Senior Director, Qualcomm Incorporated, San Diego, California, United States. Title: AI and computer vision Venue: F-202 (F-Block 1st Floor) https://meet.google.com/hpp-qqfi-dtq</p>	<p>Keynote Session 5 Dr. Parag Paul, Founder and Architect, RIG AI, Seattle, Washington, United States. Title: Designing Databases for Generative AI: From Static Storage to Adaptive Knowledge Systems Venue: F-203 (F-Block 1st Floor) https://meet.google.com/jsg-xsyn-doa</p>		
9.45 am – 10.30 am	<p>Keynote Session 6 Dr. S. Sundar Kumar Iyer, Professor, Department of EE, Indian Institute of Technology Kanpur, Kanpur. Title: The Fascinating World of Flexible Electronics Venue: F-202 (F-Block 1st Floor) https://meet.google.com/hpp-qqfi-dtq</p>	<p>Keynote Session 7 Dr. Sabarimalai Manikandan, Associate Professor, Department of EE, Indian Institute of Technology Palakkad, Kerala. Title: IoT and AI for Resource-Efficient Edge Monitoring Venue: F-203 (F-Block 1st Floor) https://meet.google.com/jsg-xsyn-doa</p>		
8.30am – 10.30am	<p>Technical Presentation 2</p> <table border="1" style="width: 100%; text-align: center;"> <tr> <td data-bbox="449 1224 1257 1409"> <p>Session 2A Venue: PSG-TI Centre of Excellence for Signal Processing (E-Block 2nd Floor)</p> </td> <td data-bbox="1257 1224 1967 1409"> <p>Session 2B Venue: ECE Conference Hall (E-Block 2nd Floor)</p> </td> </tr> </table>		<p>Session 2A Venue: PSG-TI Centre of Excellence for Signal Processing (E-Block 2nd Floor)</p>	<p>Session 2B Venue: ECE Conference Hall (E-Block 2nd Floor)</p>
<p>Session 2A Venue: PSG-TI Centre of Excellence for Signal Processing (E-Block 2nd Floor)</p>	<p>Session 2B Venue: ECE Conference Hall (E-Block 2nd Floor)</p>			
10.30am - 10.45am	Interaction Break			

10.45am – 12.45pm	Technical Presentation 2		
	Session 2C Venue: F-202 (F-Block 1 st Floor)	Session 2D Venue: F-203 (F-Block 1 st Floor)	Session 2E Venue: SMDP C2S VLSI Design Centre (E-Block 2 nd Floor)
	Session 2F Venue: PSG-TI Centre of Excellence for Signal Processing (E-Block 2 nd Floor)	Session 2G Venue: ECE Conference Hall (E-Block 2 nd Floor)	Session 2H Venue: PSG-Keysight Centre of Excellence for Advanced Wireless Technology (E-Block 2 nd Floor)
12.45pm – 1.30pm	Networking Lunch Break		
1.30pm – 3.00pm	Technical Presentation 3		
	Session 3A Venue: F-202 (F-Block 1 st Floor)	Session 3B Venue: F-203 (F-Block 1 st Floor)	Session 3C Venue: SMDP C2S VLSI Design Centre (E-Block 2 nd Floor)
	Session 3D Venue: PSG-TI Centre of Excellence for Signal Processing (CESP) (E-Block 2 nd Floor)	Session 3E Venue: ECE Conference Hall (E-Block 2 nd Floor)	Session 3F Venue: PSG-Keysight Centre of Excellence for Advanced Wireless Technology (E-Block 2 nd Floor)
3.00pm – 3.30pm	Interaction Break		
3.30pm - 4.30pm	ICIET 25 – Valedictory Function & Certificate Distribution Venue: New Admin Block Conference Hall https://meet.google.com/hpp-qqfi-dtq Chief Guest Dr. M. Saravanan, Principal Researcher, Ericsson Research India, Bengaluru.		